DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION

	() Original	() Sup	plemental () Substitut	e (y) PC	T () Design	
to my nan	as a below named inventor, I ne; that I verily believe that ator (if plural inventors are na	I am the orig	inal, first and so	ole inventoi	(if only one r	name is listed below) o	or an original, first and
Title:	Semiconductor	Device	Package	Manuf	acturin	g Method and	
	Semiconductor	Device	Package	Manuf	actured	by the Meth	ıod
() the a () the s and v (v) the s on I hereby any and adefined in it Lhereby of patential	is described and claimed in: attached specification, or specification in the application with amendments through specification in International A	application No applicable). nd understan ve. the Patent Regulations, Fitle 35, Unit ed below and	d the content of and Trademark §1.56. ed States Code, I have also iden	plicable), o 0 / 0 4 6 9 of the above Office all i , §119 (and tified below	r 9, filed e-identified sp nformation kn §172 if this ap	July 13, 20 ecification, including the own to me to be material optication is for a Design	he claims, as amended erial to patentability as
7	COUNTRY	A	PPLICATION I	NO.	DA	TE OF FILING	PRIORITY CLAIMED
Jar	pan	11-	202847		July :	16, 1999	YES
Jar	pan	200	0-63686			8, 2000	YES
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		<u> </u>					
subject m the first p ed in Titl	claim the benefit under Title natter of each of the claims o paragraph of Title 35, United S le 37, Code of Federal Regu rnational filing date of this a	f this applicat States Code, lations, §1.56	tion is not disloc §112, I acknowl	osed in the ledge the di	prior United S ity to disclose	States application in the information material to	ne manner provided by o patentability as defin-
A	PPLICATION SERIAL NO.		U.S. FILI	NG DATE			TED, PENDING, NDONED
I							

1	And I hereby appoint John T. Miller, Reg. No. 21,120; Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Jeffrey Nolton, Reg. No. 25,408; Warren M. Cheek, Jr., Reg. No. 33,367; Nils E. Pedersen, Reg. No. 33,145 and Charles R. Watts, Reg. No. 33,142, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., attorneys to prosecute this application
	and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys named herein to accept and follow instructions from Aoyama & Partners

as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

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statements may jeop	ardize the validity of the application or any p	patent issuing thereon.	Date 22/11/2001				
st Inventor Nori	hito TSUKAHARA Tahashi A	himana	Date 22/11/200/				
	enori MIYAKAWA Hidenovi Miyakawa	akawa	Date 22/11 / 200/				
		•	Date				
5th Inventor			Date				
6th Inventor	A		Date				
7th Inventor			Date				
•							
The above ap	plication may be more particularly identified	as follows:					
U.S. Application Se	U.S. Application Serial No Filing Date						
Applicant Reference Number 534653 Atty Docket No.							
Title of Invention	Semiconductor Device P.	ackage Manufa	cturing Method and				
	Semiconductor Device P	ackage Manufa	ctured by the Method				